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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	
				Filing Date	
				First Named Inventor	
				Art Unit	
				Examiner Name	
Sheet 1 of 1				Attorney Docket Number CML00770D	

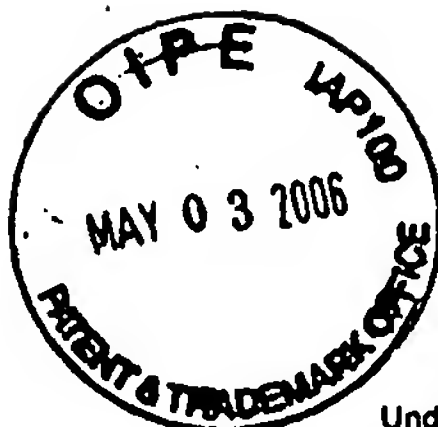
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PTO/SB/08B (10-01)
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Substitute for form 1449B/PTO		Complete If Known			
		Application Number	10/652,134		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	3/29/2003		
		First Named Inventor	May et al.		
		Art Unit	2185		
		Examiner Name	Daniel H. Pan		
Sheet	1	of	1	Attorney Docket Number	CML00770D

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
h		LAM, M., Software Pipelining: An Effective Scheduling Technique for VLIW Machines, Proceedings of the SIGPLAN '88 Conference on Programming Language Design and Implementation, Atlanta, Georgia, June 22-24, 1988, pp. 318-328	
h		LEE, T. et al., A Transformation-Based Method for Loop Folding, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 13, No. 4, April 1994, pp. 439-450	
h		KAVI, K.M. et al., A Formal Definition of Data Flow Graph Models, IEEE Transactions on Computers, Vol C-35, No. 11, Nov 1986, pp. 940-948	
h		COOPER, K.D. et al., Efficient Computation of Flow Insensitive Interprocedural Summary Information, SIGPLAN Notices, Vol. 19, No. 6, June 1984, pp. 247-258	
h		STRONG, H.R., Vector Execution of Flow Graphs, Journal of the Association for Computing Machinery, Vol 39, No. 1, January 1983, pp. 186-196	
h		AIKEN, A. et al., Resource-Constrained Software Pipelining, IEEE Transactions on Parallel and Distributed Systems, Vol. 6, No. 12, December 1995, pp. 1248-1270	
h		WULF, WILLIAM A. "Evaluation of the WM Architecture." Proceedings of the 19 th Annual International Symposium on Computer Architecture: 1992, pp. 382-390, ACM 0-89791-509-7/92/0005/0382.	
h		TALLA, D., Architectural techniques to Accelerate Multimedia Applications on General-Purpose Processors, Dissertation, August 2001, pp. 94-125	
h		TALLA, D., Bottlenecks in Multimedia Processing with SIMD Style Extensions and Architectural Enhancements, IEEE Transactions of Computers, August 2003, pp. 1015-1031, Vol. 62, No. 8, IEEE	

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete If Known	
				Application Number	10/652,134
				Filing Date	3/29/2003
				First Named Inventor	May et al.
				Art Unit	2185
				Examiner Name	Daniel H. Pan
				Attorney Docket Number	CML00770D
Sheet	1	of	2		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.†	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code² (if known)			
h		3,718,912	Feb-73	Hasbrouck et al.	
h		4,128,880	Dec-78	Cray Jr.	
h		4,744,043	May-88	Kloker	
h		4,760,545	Jul-88	Inagami et al.	
h		4,807,183	Feb-89	Kung et al.	
h		4,825,361	Apr-89	Omoda et al.	
h		4,918,600	Apr-90	Harper et al.	
h		5,206,822	Apr-93	Taylor	
h		5,390,352	Feb-95	Kinoshita	
h		5,450,607	Sep-95	Kowalczyk et al.	
h		5,495,617	Feb-96	Yamada	
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h		5,966,528	Oct-99	Wilkinson et al.	
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h		5,999,736	Dec-99	Gupta et al.	
h		6,052,766	Apr-00	Betker et al.	
h		6,064,819	May-00	Franssen et al.	
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h		6,192,384	Feb-01	Daily et al.	
h		6,437,804	Aug-02	Ibe et al.	

FOREIGN PATENT DOCUMENTS						
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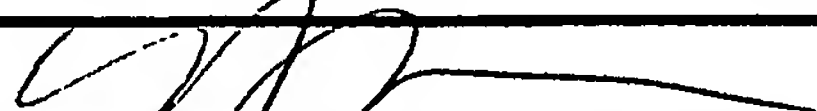

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				Filing Date	3/29/2003
				First Named Inventor	May et al.
				Art Unit	2185
				Examiner Name	Daniel H. Pan
Sheet	2	of	2	Attorney Docket Number	CML00770D

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h		5,697,788	Dec-97	Ohta	
h		5,717,947	Dec-98	Gallup et al.	
h		5,719,998	Feb-98	Ku et al.	
h		5,764,787	Jun-98	Nickerson	
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h		6,202,130	Mar-01	Scales et al.	
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h		6,513,107	Jan-03	Ansari	
h		6,588,009	Jul-03	Guffens et al.	
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h		20050055543	Mar-05	Moyer	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10652134
	Filing Date		2003-08-29
	First Named Inventor	MAY, Philip E.	
	Art Unit	2183	
	Examiner Name	PAN, Daniel H.	
	Attorney Docket Number	CML00770D	

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10652134
Filing Date	2003-08-29
First Named Inventor	MAY, Philip E.
Art Unit	2183
Examiner Name	PAN, Daniel H.
Attorney Docket Number	CML00770D

	¹	HENNESSY and PATTERSON, "Computer Architecture A Quantitative Approach, 1996, Morgan Kaufman Publishers, Inc., Second Edition, pages 239 - 247.	<input type="checkbox"/>
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